

SEP 17 2004

501.41261X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshiyuki ARAI *et al.*
Application No.: 10/085,083
Filed: March 1, 2002
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 1765
Examiner: B.X. Tran
Conf. No.: 9140

PETITION FOR EXTENSION OF TIME UNDER 37 CFR §1.136

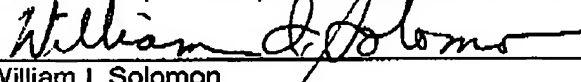
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

17 September 2004

Sir:

Applicants respectfully petition the Commissioner for an appropriate (1-month) extension of the shortened statutory period for response set by the Office Action mailed 17 May 2004 in the above-identified application. The requisite fee is attached hereto by Credit Card Payment Form PTO-2038. Please charge any actual shortage in the fees due in connection with the filing of this paper to ATS&K Deposit Account No. 01-2135 (as Order No. 501.41261X00), and please credit any overpayment thereto.

ANTONELLI, TERRY, STOUT & KRAUS, LLP


William I. Solomon
Registration No. 28,565

1300 North Seventeenth Street
Suite 1800
Arlington, Virginia 22209-3812
Telephone 703-312-6600
Facsimile 703-312-6666

09/20/2004 MAHMED1 00000045 10085063

01 FC:1251

110.00 OP